

Electronic Acknowledgement Receipt

EFS ID:	3012743
Application Number:	10783589
International Application Number:	
Confirmation Number:	1594
Title of Invention:	Regulating unused/inactive resources in programmable logic devices for static power reduction
First Named Inventor/Applicant Name:	Kevin T. Look
Customer Number:	24309
Filer:	Justin Liu/Katherine Stofer
Filer Authorized By:	Justin Liu
Attorney Docket Number:	X-1462-2P US
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /zip	Pages (if appl.)
1	Information Disclosure Statement Letter	X1462_2P_US_Sup_IDS_efi led.pdf	51048 4ba2708b982c3ef78c3dfa52073994c6 4966830	no	1

Warnings:

Information:

2	NPL Documents	Allen_Holberg_Bandgap_Voltage_NPL.pdf	65302 4789608185481277e1d8135900ce99 9e599e02	no	5
Warnings:					
Information:					
3	NPL Documents	Intel_PXA27X_Proc_Fam_Pwr_Req_AppNote_2004_NPL.pdf	583969 99e5404270e7e2e000737534591103c 01b41a26	no	36
Warnings:					
Information:					
4	NPL Documents	Microchip_Tech_Micropower_Voltage_NPL.pdf	698019 d02dd71492104a1793:0799b61:0bc 910e927	no	28
Warnings:					
Information:					
5	NPL Documents	Nowka_etal_A_32_bit_PowerPC_NPL.pdf	552946 2a7741babb62a66ea4d0dd2e25590de 242ea106a	no	7
Warnings:					
Information:					
6	NPL Documents	Texas_Instruments_DS_BQ_4011_05_99_NPL.pdf	373289 d5b7404b800d4d4319c4ca7d110c1 347d9cb	no	15
Warnings:					
Information:					
7	NPL Documents	Texas_Instruments_DS_BQ_4011_08_93_NPL.pdf	235384 3e099e912e56e866c06ea3c78156462 a3042e0d	no	11
Warnings:					
Information:					
8	NPL Documents	Xilinx_DS313_Spartan_3_Low_Power_FPGA_NPL.pdf	377709 e6bd0dd9f7c94894aefb4dca824eb04 0a3b1a5	no	10
Warnings:					
Information:					
9	NPL Documents	Virtex_Two_Pro_FPGA_NPL.pdf	2803551 f5ea580114a4b9514e3e486d522b10a 9420409c	no	56
Warnings:					
Information:					
10	Information Disclosure Statement (IDS) Filed	X1462_2P_US_SB08A_efiled.pdf	112847 8ea21f187c72bd50787292056de82a9 ec0a1110	no	4
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.